

Team Leader: Tom Blanford

Secretary: Eric Nolan

Unresolved Items from Previous Meeting:

- Integrate ADC and DSP
- Board design and layout

Current Items:

1. Clock
  - a. DSP can output required clock
  - b. Serial data in pins must be slave, so DSP can act as master and output master clock over BCLK
2. Battery/Charging circuitry
  - a. Charging chip samples ordered and on their way
  - b. 10 pin DFN pinout boards to test charging circuit?
3. ADC
  - a. ADC progress update
    - i. Works but is distorted and noisy
    - ii. 8 pin ribbon cable and connector - possible connection problems
  - b. ADC verified working and outputting data for audio input
  - c. Resistors between ADC and DSP?
  - d. Cleaned up digital data, but still may be dirty at DSP
  - e. Clean up overshoot with 50 or 100 ohm resistor and driver source
4. User Interface
  - a. Cannot switch on/off music just by switching ground.
  - b. Sara has layout and all switches selected.
5. Voltage Regulator
  - a. Eric has schematic for voltage regulators
6. Board Design
  - a. Preliminary board design and layout will be completed tonight.