Vision Group

3/20 Minutes

Camera

- Iris adjusts for lighting
- White dot tracking test mode
- Use camera as black box
- Raw image comes out of camera (eg: 680x480 pixels) => can convert

Algorithms

- Having trouble with format (video vs. still image)
- Frame by frame is one option
- Background image then tracking edges
- Split up tracking and time
- Fit in FPGA, running in parallel?
- MATLAB => XILINX a possibility
- Arrangement of camera, FPGA, and microcontroller
- Bitmap output => go directly into FPGA
- Are we going to need something like serial flash for more memory for frames

FPGA

- Met with Brockman
- Algorithm can help determine FPGA

Communication

- Addressing info
- Make sure SPI is working by using logic analyzer
- Look for problems (eg: JTAG vs. IO, analog pins, etc)

Board

- Haven't started design, but beginning research