

Agenda 23JAN2019

Meeting Ran by: Adam Burt

Minutes Taken by: Mark Etzelmueller

- ☐ Define timeline of project
- ☐ Define task division
- ☐ Updates on tasks
 - ☐ Website
 - ☐ Have FTP working
 - ☐ Currently working on design and layout
 - ☐ RSL10 Communication:
 - ☐ Sample code is working (LED Blinking)
 - ☐ Next step will be getting voltage output program working
 - ☐ LEDs
 - ☐ LEDs selected for Prof O'Sullivan's review (both through hole and surface mount)
 - ☐ MATLAB Program
 - ☐ Reading in txt files
 - ☐ Investigating possible power options
- ☐ Questions for Schafer
 - ☐ Is it best for us to try to use the RSL10 as our main microcontroller going forward or should we also be considering drop-in chips (like the ESP part for WiFi, etc.) and use the PIC?
 - ☐ Should we attempt to have a board with the LEDs on it or try to find through-hole parts that we can break out to a breadboard using our upcoming AFE board?

Timeline of Tasks:

- ☐ Have website up by 1/24/19
- ☐ Have AFE working by 2/6/19
- ☐ Compile list of potential issues with Bluetooth, Diodes, and MATLAB by 2/1/19
- ☐ Have MATLAB code working 2/15/19
- ☐ Have working Bluetooth connection 2/22/19
- ☐ Resolve remaining issues with AFE 3/1/19
- ☐ Have working diodes and photodetector 3/6/19
- ☐ Have MATLAB and bluetooth connections working together 3/22/19
- ☐ Have diodes and AFE board working together 3/22/19
- ☐ Refine MATLAB program with testing with diode 3/27/19
- ☐ Have completed Bill of Material 3/27/19