

Senior Design Meeting Agenda — Microgrid Team

10:00AM Thursday, April 1st, 2021

205 Stinson Remick

Meeting Leader: *Brian*

Secretary: *Dan*

Board Review

- Fully routed and ready to order
- Power Chip issues
 - Unable to effectively communicate with power chip
 - Built in the contingency to monitor loads with analog pins. We could not initially do this because of AC system but DC allows us to use analog pins to measure the voltage across the loads/generations/battery

Build Plan

- Full System Construction: end to end construction of microgrid
 - Using boards from last semester to ensure functionality of control system logic
 - Sized voltage dividers to step down voltage to the microcontroller level
 - Check power chip with finalized board design
 - Building has begun